

**GOOD-ARK Electronics** 

# 2A,20-40V Schottky Barrier Rectifiers

#### **Features**

- Low leakage current
- Schottky barrier diodes
- Low forward voltage drop
- Moisture sensitivity: level 1, per J-STD-020
- Halogen-free according to IEC 61249-2-21 definition
- High temperature soldering guaranteed: 260 ℃/10 seconds



## **Applications**

For use in low voltage, high frequency inverters, free-wheeling and polarity protection application.

Maximum Ratings & Electrical Characteristics(TA=25°C unless otherwise noted)					
Parameter	Symbol	Symbol FS22 FS23 FS2		FS24	Unit
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	20	30	40	V
Maximum RMS voltage	V <sub>RMS</sub>	14	21	28	V
Maximum DC blocking voltage	V <sub>DC</sub>	20	30	40	V
Maximum average forward rectified current	I <sub>F(AV)</sub>	2			Α
Peak forward surge current,8.3ms single half sine- wave superimposed on rated load per diode	IFSM	50			А
Operating junction temperature range	TJ	-55 to +150			°C
Storage temperature range	T <sub>STG</sub>	-55 to +150			°C

Thermal-Mechanical Specifications (TA=25°C unless otherwise noted)				
Parameter	Symbol	Тур	Unit	
Thermal Resistance, Junction to Ambient	R <sub>θ</sub> JA	100	°C /W	
Thermal Resistance, Junction to Case	Reuc	20	°C /W	
Thermal Resistance, Junction to Lead	R <sub>eJL</sub>	20	°C /W	



Electrical Specifications(TA=25°C unless otherwise noted)						
Parameter	Symbol	Test Conditions	FS22	FS23	FS24	Unit
Forward Drop Voltage	V <sub>F</sub>	I <sub>F</sub> =1A	0.50			- V
		I <sub>F</sub> =2A	0.55			
Reverse leakage current @V <sub>R</sub>	I <sub>R</sub>	T <sub>J</sub> =25°C	0.2			- mA
		T <sub>J</sub> =125°C	30			
Typical junction capacitance	С	4.0 V 1 MHZ	120		pF	

#### Note:

1. Mounted on copper pad area of 0.2x0.2" (5.0 x 5.0mm) to each terminal.





### **Ratings and Characteristics Curves**

(TA = 25°C unless otherwise noted)

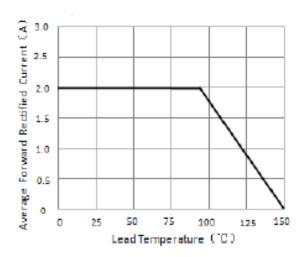


Figure 1.Forward Current Derating Curve

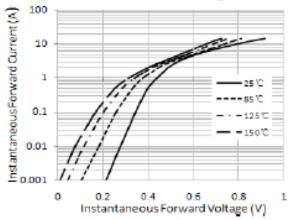


Figure 3. Typical Instantaneous Forward Characteristics

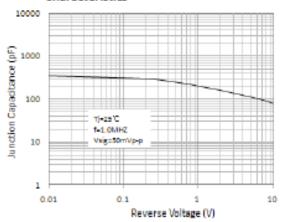


Figure 5. Typical Junction Capacitance

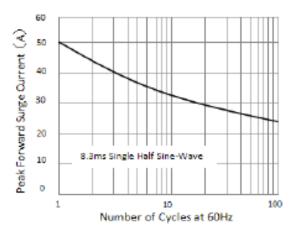


Figure 2.Maximum Non-Repetitive Peak Forward Surge Current

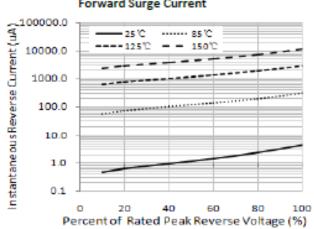


Figure 4. Typical Reverse Characteristics

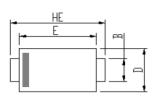


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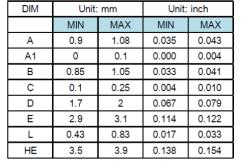
## **Package Outline Dimensions**

in inches (millimeters)

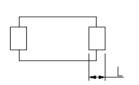
# eSGA (SOD-123FL)



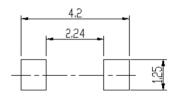








Soldering footprint



# **Revision History**

Document Version	Date of release	Description of changes
Rev.A	2021.06.01	Released Datasheet
Rev.B	2023.10.11	Modify document format



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